

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Kiyoshi Mita	Art Unit	: 1753
Serial No.	: 10/813,778	Examiner	: Luan V. Van
Filed	: March 31, 2004	Conf. No.	: 5229
Title	: METHOD FOR MANUFACTURING MOUNTING SUBSTRATE AND METHOD FOR MANUFACTURING CIRCUIT DEVICE		

**Mail Stop RCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Please amend the application as indicated on the following pages.